	Туре	L	#	Hits	Search Text	DBs	Time Stamp	Comment s
1	BRS	L1		8171	wafer and electrostatic	USPA T	2003/04/2 8 08:51	
2	BRS	L2		816	semiconductor adjl wafer and electrostatic adjl (carrier or chuck)	USPA T	2003/04/2 8 08:55	
3	BRS	L3		1	semiconductor adjl wafer and electrostatic adjl carrier	USPA T	2003/04/2 8 08:55	
4	BRS	L4			semiconductor adj1 wafer and electrostatic adj1 chuck and method and process\$3		2003/04/2 8 08:59	
5	BRS	L5		282	semiconductor adj1 wafer same electrostatic adj1 chuck and method same process\$3	USPA T	2003/04/2 8 09:00	
6	BRS	L6		0	semiconductor adj1 wafer same (movable or moving or transporting or transportable) adj2 electrostatic adj1 chuck and method same process\$3		2003/04/2 8 09:01	